

Certificate ID	SESIP-2500191-01
	<i>TrustCB B.V. declares that</i>
Product	STM32H5Fxxx, Version 1.1
	<i>of</i>
Sponsor (and Developer)	STMicroelectronics 12 rue Jules Horowitz, 38000 Grenoble, FRANCE
	<i>complies to the requirements described in Standard and ST Reference</i>
Standard	GlobalPlatform Technology, Security Evaluation Standard for IoT Platforms (SESIP), GP_FST_070, Public Release v1.1, June 2021 Based on ISO/IEC 15408-1:2009, -2 :2008, -3:2008, Common Criteria for Information Technology Security Evaluation (CC v3.1, rev 5), and ISO/IEC 18045:2008, Common Criteria Evaluation Methodology for Information Security Evaluation (CEM) v3.1, rev 5
	
ST Reference	Technical note STM32H5Fxxx security target for security services (ST0061), Revision 1.0
	<i>Summarised:</i>
Assurance Package	SESIP3 <i>with</i> Physical Attacker Resistance Software Attacker Resistance – Isolation of Platform
SESIP Profile	SESIP protection profile for secure MCUs and MPUs, version 1.0 (TOE configured with STIROT) SESIP profile for PSA certified RoT component level 3, version 1.0 REL 01 (TOE configured with or without STIROT)
	<i>As evaluated by:</i>
Evaluation Facility	SGS Brightsight located in Delft, The Netherlands
	<i>Under scheme:</i>
Scheme	SESIP <i>As described in</i> TrustCB Scheme Procedures – SESIP v2.6
Validity	Date of issue: 2026-05-13 Date of expiry: 2031-04-10
Certification Mark	
	
	
Signatory	Wouter Slegers, CEO