

Certificate ID	SESIP-2500176-01	
	<i>TrustCB B.V. declares that</i>	
Product	STM32C5 Series advanced Arm®-based 32-bit MCUs, STM32C542xx, revision Z, platform identification 0x44F STM32C562xx, revision Y, platform identification 0x44E STM32C5A3xx, revision Z, platform identification 0x45A	
	<i>of</i>	
Sponsor (and Developer)	STMicroelectronics 12 rue Jules Horowitz, 38000 Grenoble, FRANCE	
	<i>complies to the requirements described in Standard and ST Reference</i>	
Standard	GlobalPlatform Technology, Security Evaluation Standard for IoT Platforms (SESIP), GP_FST_070, Public Release v1.2, July 2023 Based on ISO/IEC 15408-1:2009, -2 :2008, -3:2008, Common Criteria for Information Technology Security Evaluation (CC v3.1, rev 5), and ISO/IEC 18045:2008, Common Criteria Evaluation Methodology for Information Security Evaluation (CEM) v3.1, rev 5	
		
ST Reference	STM32C5xxxx SESIP security target (ST0063), revision 1.0	
	<i>Summarised:</i>	
Assurance Package	SESIP3 <i>with</i> Physical Attacker Resistance <i>and</i> Software attacker resistance: Isolation of platform	
SESIP Profile	SESIP Profile for PSA Certified™ Level 3 iSE/SE and RoT Component, version 2.0 REL 01	
	<i>As evaluated by:</i>	
Evaluation Facility	Thales SIX GTS France located in Labège, France	
	<i>Under scheme:</i>	
Scheme	SESIP <i>As described in</i> TrustCB Scheme Procedures – SESIP v2.6	
Validity	Date of issue:	2026-04-10
	Date of expiry:	2031-03-11
Certification Mark	 	
Signatory	Wouter Slegers, CEO	