


Certificate ID	SESIP-2400007-01	
	<i>TrustCB B.V. declares that</i>	
Product	STM32U0 ultra-low-power Arm® Cortex®-M0+ 32-bit MCU Revision 1	
	<i>of</i>	
Sponsor (and Developer)	STMicroelectronics <i>in</i> Grenoble, France	
	<i>complies to the requirements described in Standard and ST Reference</i>	
Standard	<p>GlobalPlatform Technology, Security Evaluation Standard for IoT Platforms (SESIP), GP_FST_070, Public Release v1.1, June 2021</p> <p><b>Based on</b> ISO/IEC 15408-1:2009, -2 :2008, -3:2008, Common Criteria for Information Technology Security Evaluation (CC) v3.1, rev 5, and ISO/IEC 18045:2008, Common Criteria Evaluation Methodology for Information Security Evaluation (CEM) v3.1, rev 5</p>	
ST Reference	STM32U0 SESIP Security Target for PSA Certified™ RoT Component Level 3 (TN1481) revision, March 2024	
	<i>Summarised:</i>	
Assurance Package	SESIP3 <i>with</i> Physical Attacker Resistance <i>and</i> Software Attacker Resistance: Isolation of Platform	
SESIP Profile	SESIP Profile for PSA Certified RoT Component Level 3 Version 1.0 Rel02	
	<i>As evaluated by:</i>	
Evaluation Facility	SGS Brightsight located in Delft, The Netherlands	
	<i>Under scheme:</i>	
Scheme	SESIP <i>As described in</i> TrustCB Scheme Procedures v2.3	
Validity	Date of issue:	2024-04-24
	Date of expiry:	2026-03-15
Certification Mark	 	
Signatory	Wouter Slegers, CEO	